## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

APPARATUS AND METHOD FOR ELECTRONIC FUSE WITH IMPROVED ESD TOLERANCE

Application Number : Confirmation Number:

First Named Applicant: Steven Voldman
Attorney Docket Number: BUR920030119US1

Art Unit: Examiner:

Search string: (4515981 or 5936880 or 6580156 or 6525397 or 6088256 or 6368902 or 6507087

or 4748491 or 20030025177 or 20020102839 or 20020086462 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4515981	1985-05-21	Schlupp			
	2	5936880	1999-08-10	Payne			
	3	6580156	2003-06-17	Ito et al.			
	4	6525397	2003-02-25	Kalnitsky et al.			
	5	6088256	2000-07-11	Worley et al.			
	6	6368902	2002-04-09	Kothandaraman			
	7	6507087	2003-01-14	Yu			
	8	4748491	1988-05-31	Takagi			

## **US Published Applications**

Note: Applicant is not required to submit a paper copy of cited US Published Applications

Γ	init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
Γ		1	20030025177	2003-02-06	Kothandaraman			
Γ		2	20020102839	2002-08-01	Gonzalez et al.			
ſ		3	20020086462	2002-07-04	Kothandaraman			
					et al.			

## **Signature**

	Examiner Name	Date		
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